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1765 #

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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June 12, 2006

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Harold C. Moore

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June 12, 2006

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Re: Application of: Charles E. May
Serial No.: 09/943,196
Filed: August 30, 2001
Confirmation No.: 8500
For: Arrangement and Method for
Fabricating a Semiconductor Wafer
Group Art Unit: 1765
Examiner: Lynette T. Umez Eronini
Our Docket No.: 1003-0606

PETITION FOR EXTENSION OF TIME

PURSUANT TO 37 C.F.R. § 1.136(a)

Sir:

In connection with the Office Action mailed January 11, 2006 in the above-entitled patent application, Applicant respectfully requests a two (2) months extension in which to file a response, from April 11, 2006 to June 11, 2006.

Since the due date of filing the Response to Office Action fell on Sunday, June 11, 2006, the Response to Office Action is being timely filed on Monday, June 12, 2006.

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450.00 0P

The \$450.00 fee required by 37 C.F.R. § 1.17(a)(1) is submitted herewith.

Respectfully Submitted,

A handwritten signature in black ink, appearing to be 'H. C. Moore', written over the printed name.

Harold C. Moore
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June 12, 2006
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